



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/803,203 Confirmation No.: 5841
First Named Inventor: Bulucea, Constantin Filing Date: 17 March 2004
Group Art Unit: 2826 Examiner: Quach, T. N.
Atty. Docket No.: NS-5737 US
Title: Configuration and Fabrication of Semiconductor Structure Having
N-channel Channel-junction Field-effect Transistor
Assignee(s): National Semiconductor Corporation

Mountain View, California
8 December 2006

**MAIL STOP ISSUE FEE
COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, Virginia 22313-1450**

AMENDMENT TO TEXT UNDER 37 CFR 1.312

Sir:

Responsive to the Notice of Allowance mailed 11 September 2005 and under the provisions of 37 CFR 1.312, please amend the above patent application as described below.

Ronald J. Meetin
Attorney at Law
210 Central Avenue
Mountain View, CA
94043-4869
Tel.: 650-964-9767
Fax: 650-964-9779